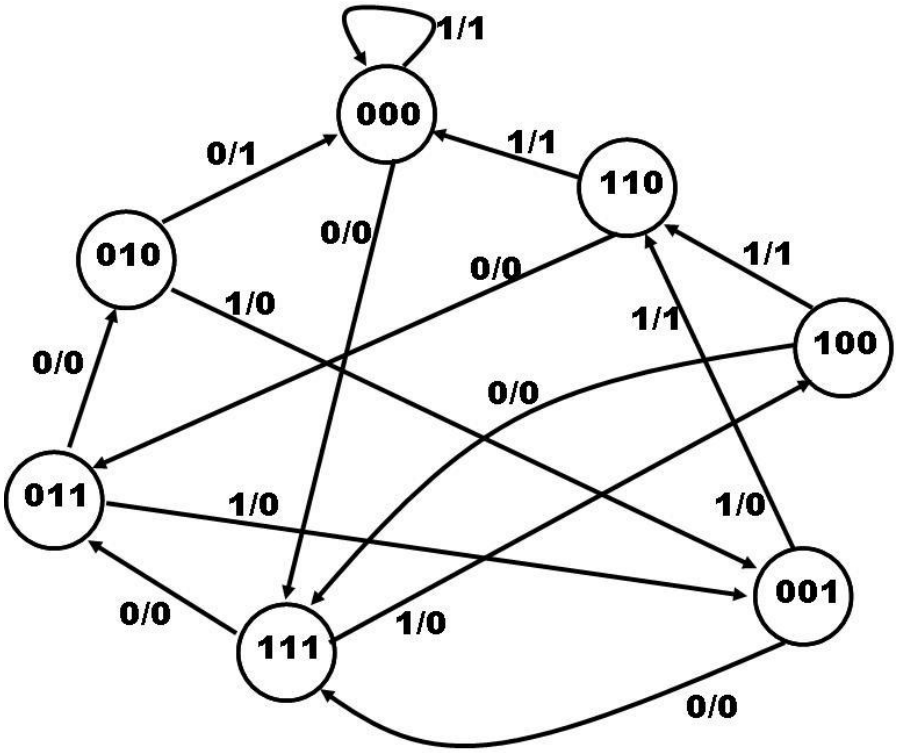
UNIVERSITY OF THESSALY

ELECTRICAL AND COMPUTER ENGINEERING DEPARTMENT

DIGITAL DESIGN

6th Homework Set

Implement the circuit described by the following state diagram with D-type flip-flops:



a) using AND/OR/NOT gates

b) with a 4-to-16 decoder and two-input OR gates

c) with 4-to-1 multiplexers